

PTO/SB/08A (04-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	09/749,281
Filing Date	December 26, 2000
First Named Inventor	Gene R. Anderson
Art Unit	2872
Examiner Name	Cuneo, Kamand
Attorney Docket Number	1613370-0017

Attorney Docket Number 1613370-0017

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages Or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			
901		DE 41 23 370 A1	01-21-1993	Robock, Wilfried	
		EP 1 028 341 A2	08-16-2000	Hewlett-Packard Co.	
		JP 56 111822	03-09-1981	Oki Densen KK	
		WO 00/46623	08-10-2000	Siemens	
		WO 00/73833 A1	10-07-2000	E20 Comm., Inc.	

Examiner

Jeremy Morris

Date

3-15-03

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Sheet	2	of	3	Attorney Docket Number	1613370-0017
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Application Number	09/749,281
Filing Date	December 26, 2000
First Named Inventor	Gene R. Anderson
Art Unit	2827
Examiner Name	Cuneo, Kamand

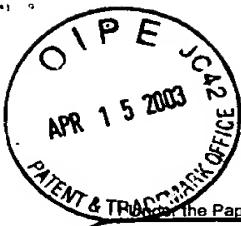
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<i>GR</i>		1 Gb/s VCSEL/CMOS Flip-Chip 2-D-Array Interconnects and Associated Diffractive Optics; SIMONIS et al.; Army Research Laboratory, Adelphi, MD; University of Maryland, MD; University of Delaware, DE; pp. 1-9		
		Power Minimization and Technology Comparisons for Digital Free-Space Optoelectronic Interconnections; KIBAR et al.; Journal of Lightwave Technology, Vol. 17, No. 4; April 1999; pp. 546-555		
		Low Cost Molded Packaging for Optical Data Links; ROBINSON et al.; AT&T Bell Laboratories; copyright 1994 IEEE; pp. 312-316		
		1-Gbyte/sec Array Transmitter and Receiver Modules for Low-cost Optical Fiber Interconnection; NAGAHORI et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 255-258		
		A Complete Sub-System of Parallel Optical Interconnects for Telecom Applications; NIBURG, et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 259-263		
		Parallel Optical Interconnections for Future Broad Band Systems, based on the "Fibre in Board Technology"; de PESTEL et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 264-266		
		Optoelectronic Technology Consortium (OETC) Parallel Optical Data Link: Components, System Applications, and Simulation Tools; WONG et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 269-278		
		Characteristics of VCSEL Arrays for Parallel Optical Interconnects; LEBBY et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 279-291		
		The Jitney Parallel Optical Interconnect; CROW et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 292-300		
		Gigabyte/s Data Communications with the POLO Parallel Optical Link; HAHN et al.; 1996 Electronic Components & Technology Conference, 46th Proceedings, pp. 301-307		

Examiner Signature	<i>Jeremy Morris</i>	Date Considered	<i>5-15-03</i>
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				<p>Application Number 09/749,281</p>
				<p>Filing Date December 26, 2000</p>
				<p>First Named Inventor Gene R. Anderson</p>
				<p>Art Unit 2872</p>
				<p>Examiner Name Cuneo, Kamand</p>
Sheet	3	of	3	Attorney Docket Number
				1613370-0017

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